

Amendments to the Claims

The listing of claims will replace all prior versions, and listings of claims in the application.

1. (Currently Amended) A broadcast television system committee (BTSC) decoder, comprising:
 - an intermediate frequency (I/F) demodulator configured to convert and demodulate received I/F signals to digital audio samples, an output of the I/F demodulator being scalable;
 - a digital signal processor (DSP) configured to BTSC-decode the digital samples; and
 - an all digital interface that couples the I/F demodulator to the DSP;
~~wherein the digital samples are composite BTSC samples formatted in accordance with multi-channel television sound (MTS) standards.~~
2. (Original) The decoder of claim 1, wherein the received analog I/F signal is a carrier signal.
3. (Previously Cancelled)
4. (Original) The decoder of claim 1, wherein the digital interface does not include a digital to analog converter (DAC).
5. (Original) The decoder of claim 1, wherein the digital interface does not include a gain control device.

6. (Original) The decoder of claim 1, wherein the digital interface does not include an analog to digital converter (ADC).

7. (Original) The decoder of claim 1, wherein the DSP does not include an automatic gain control (AGC) device.

8. (Currently Amended) A ~~broadcast television system committee (BTSC)~~ decoder including an (i) intermediate frequency (I/F) demodulator configured for demodulating received analog I/F signals and converting the received I/F analog signals to digital form, the I/F demodulator having a scalable output and (ii) a digital signal processor (DSP) configured to process the digital signals output from the I/F demodulator, the decoder comprising:

a digital interface configured to couple the I/F demodulator and the DSP;
~~wherein the digital interface is configured to transfer digital samples that are composite BTSC samples formatted in accordance with multi-channel television sound (MTS) standards.~~

9. (Original) The decoder of claim 8, wherein the DSP does not include an automatic gain control device.

10. (Original) The decoder of claim 8, further comprising a scalable digital output.

11. (Original) The decoder of claim 8, wherein the digital interface is all digital.

12. (Original) The decoder of claim 8, wherein the digital interface does not include a digital to analog converter (DAC).

13. (Original) The decoder of claim 8, wherein the digital interface does not include an analog to digital converter (ADC).

14. (Original) The decoder of claim 8, wherein the digital interface permits the digital signal to be transferred from the I/F demodulator to the DSP in a purely digital domain.

15. (Cancelled)